Pre.	S.Uchida	Rev	B	H.Hanada
Apr.	M.Tabata 25-Sep.-'02			M.Tabata 21-Feb.-'03

Notice : This is not a final specification. Some parametric limits are subject to change.

CM150DY-12NF

- Ic 150A
- V_{CES} 600V
- Insulated Type
- 2-elements in a pack

APPLICATION

General purpose inverters & Servo controls,etc

ABSOLUTE MAXIMUM RATINGS (T_j = 25 °C)

Symbol	Item	Conditions	Ratings	Units	
V_{CES}	Collector-emitter voltage	G-E Short	600	V	
V_{GES}	Gate-emitter voltage	C-E Short	±20	V	
I_C	Collector current	DC, Tc'= 97 °C*3	150	A	B
I_{CM}		Pulse ②	300		
I_E ①	Emitter current		150	A	B
I_{EM} ①		Pulse ②	300		
Pc ③	Maximum collector dissipation	Tc= 25 °C	590	W	
Tj	Junction temperature		−40~+150	°C	
Tstg	Storage temperature		−40~+125	°C	
Viso	Isolation voltage	Main terminal to base plate,AC 1 min.	2500	V	
—	Torque strength	Main terminal M5	2.5 ~ 3.5	N·m	
—	Torque strength	Mounting holes M6	3.5 ~ 4.5	N·m	
—	Weight	Typical value	310	g	

ELECTRICAL CHARACTERISTICS (T_j = 25 °C)

Symbol	Item	Conditions		Min.	Typ.	Max.	Units	
I_{CES}	Collector cutoff current	$V_{CE}=V_{CES}, V_{GE}$= 0V		—	—	1	mA	
$V_{GE(th)}$	Gate-emitter threshold voltage	I_C=15mA, V_{CE}= 10V		5	6	7.5	V	A
I_{GES}	Gate leakage current	$V_{GE}=V_{GES}$, V_{CE}= 0V		—	—	0.5	μA	
$V_{CE(sat)}$	Collector to emitter saturation voltage	T_j= 25 °C	I_C = 150A	—	1.7	2.2	V	
		T_j= 125 °C	V_{GE}= 15V	—	1.7	—		
Cies	Input capacitance	V_{CE}= 10V		—	—	23	nF	
Coes	Output capacitance	V_{GE}= 0V		—	—	2.8		
Cres	Reverse transfer capacitance			—	—	0.9		
Q_G	Total gate charge	V_{CC}=300V, Ic=150A, V_{GE}=15V		—	600	—	nC	
td(on)	Turn-on delay time	V_{CC}=300V, Ic=150A		—	—	120	ns	
tr	Turn-on rise time	$V_{GE1}=V_{GE2}$=15V		—	—	100		
td(off)	Turn-off delay time	R_G=4.2Ω, Inductive load		—	—	300		
tf	Turn-off fall time	switching operation		—	—	300		
trr ①	Reverse recovery time	I_E=150A		—	—	150	ns	
Qrr ①	Reverse recovery charge			—	2.5	—	μC	A
V_{EC} ①	Emitter-collector voltage	I_E=150A, V_{GE}= 0V		—	—	2.6	V	
Rth(j-c)Q	Thermal resistance	IGBT part (1/2 module) *1		—	—	0.21	°C/W	
Rth(j-c)R		FWDi part(1/2 module) *1		—	—	0.47		A
Rth(c-f)	Contact thermal resistance	Case to fin, Thermal compound Applied (1/2module) *2		—	0.07	—		
Rth(j-c')Q	Thermal resistance	IGBT part (1/2 module) *3		—	—	0.16		B
RG	External gate resistance			4.2	—	42	Ω	

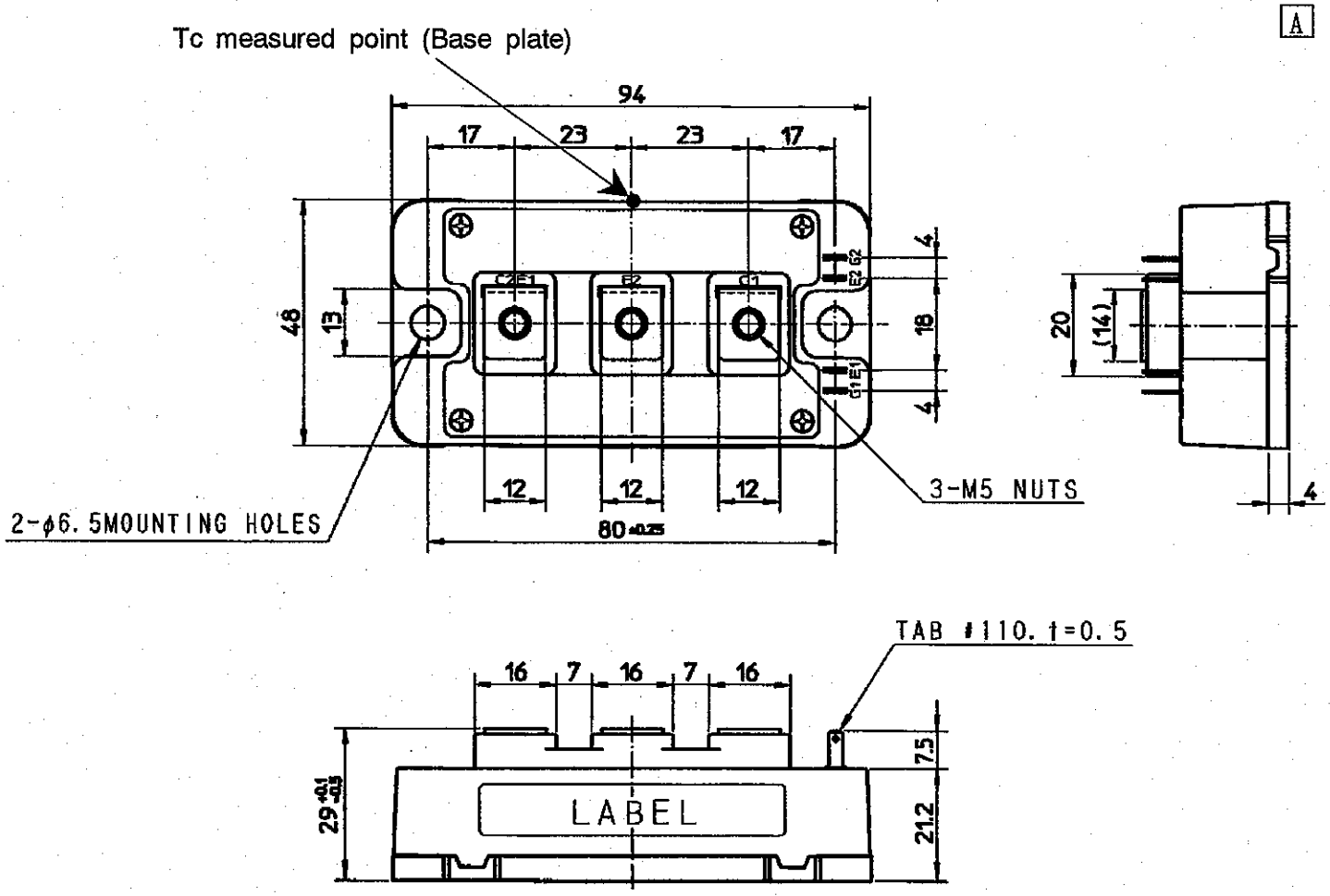
*1: Tc measured point is shown in page OUTLINE DRAWING.
*2: Typical value is measured by using Shin-etsu Silicone "G-746".
*3: Tc' measured point is just under the chips. B
If you use this value , Rth(f-a) should be measured just under the chips.

① I_E, V_{EC}, trr, Qrr & die/dt represent characteristics of the anti-parallel, emitter to collector free-wheel diode (FWDi).
② Pulse width and repetition rate should be such that the device junction temp.(T_j) dose not exceed Tjmax rating.
③ Junction temperature (T_j) should not increase beyond 150°C.
④ Pulse width and repetition rate should be such as to cause neglible temperature rise.

OUTLINE DRAWING

Dimensions in mm

A



CIRCUIT DIAGRAM